

## INFORMATION DISCLOSURE CITATION

Atty. Docket No.	08409.0025	Serial No.	10/715,558
Applicant	Tuung Luoh	APR 12 2004 JC25	
Filing Date	November 19, 2003	Group:	1754

## U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
PD	5,981,339	November 9, 1999	Chang et al.	—	—	
PD	6,383,899	May 7, 2002	Voutsas	—	—	

## FOREIGN PATENT DOCUMENTS

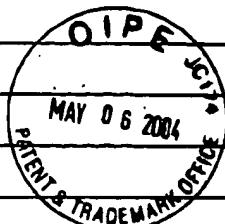
	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

PD	T.C. Ong, et al., "Erratic Erase in ETOX™ Flash Memory Array," IEEE VLSI Symposium, 1993, pp. 83-84.
PD	V. Subramanian et al., "A Novel Technique for 3-D Integration: Ge-seeded Laterally Crystallized TFTs", IEEE VLSI Symposium, 1997.

Examiner	PHUe T. DANG	Date Considered	12/8/2004
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P D	Tuung Luoh et al., "Single-Wafer Polysilicon Engineering for the Improvement of Over Erase in a 0.18- $\mu$ m Floating-Gate Flash Memory," IEEE Transactions on Semiconductor Manufacturing, Vol. 16, No. 2, May 2003, pp. 155 - 164.

Examiner	PHUONG T. DANG	Date Considered	12/8/2004
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